

VISVESVARAYA TECHNOLOGICAL UNIVERSITY
Jnana Sangama, Belagavi – 590018



Major Project Report

on

“Design and Implementation of HIGHT Algorithm in FPGA”

submitted in partial fulfillment of the requirement for the award of the degree of

BACHELOR OF ENGINEERING

in

ELECTRONICS AND COMMUNICATION ENGINEERING

by

Ms. MANIKYA

4JK21EC401

Ms. MANJUSHA M

4JK20EC025

Mr. KEERTHAN TILAK MENDON

4JK20EC023

Under the Guidance of

Mrs. PRATHEKSHA RAI

Assistant Professor



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
A J INSTITUTE OF ENGINEERING & TECHNOLOGY

A Unit of Laxmi Memorial Education Trust ®

(Approved by AICTE, New Delhi, Affiliated to Visveswaraya Technological University, Belagavi)

Kottara Chowki, Mangaluru-575006, Karnataka

Accredited by NBA (BE:CV, CSE, ECE, ISE, ME)

2023-2024

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



CERTIFICATE

Certified that the major project work entitled "DESIGN AND IMPLEMENTATION OF HIGHT ALGORITHM IN FPGA" carried out by Ms. MANIKYA (4JK21EC401), Ms. MANJUSHA M (4JK20EC025), Mr. KEERTHAN TILAK MENDON (4JK20EC023), the bonafide students of A J Institute of Engineering and Technology in partial fulfillment for the award of Bachelor of Engineering in Electronics and Communication Engineering of the Visvesvaraya Technological University, Belagavi, during the year 2023-2024. It is certified that all corrections/suggestions indicated for Internal Assessment have been incorporated in the report.

The project report has been approved as it satisfies the academic requirements in respect of major project work prescribed for the said Degree.

Signature of the Guide

Mrs. Pratheeksha Rai

External Viva:

Name of the Examiners

1. Chandra Singh
2. P. K. Shetty

Signature of the HOD

Dr. Kiran Kumar V G

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Signature of the Principal

Dr. Shantharama Rai C
Principal

A.J. Institute of Engineering & Techno
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Signature with Date
29/05/24

