



A J INSTITUTE OF ENGINEERING AND TECHNOLOGY

NH-66, Kottara Chowki, Mangaluru -575006, Karnataka, INDIA

A Unit of Laxmi Memorial Education Trust®

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A Webinar on “Opportunities in VLSI/Semiconductor Industry”

Date	18-06-2024
No. of participants	60
Resource Person	G Nagaraju, Industry Relationship Manager, Takshila VLSI

Report:

The Department Activity was organized by the department of Electronics & Communication Engineering in association with Electro Verts Student Association on 18th June, 2024 from 2:00 PM to 4:30 PM.

The objective of a workshop or discussion on "Roles and Opportunities in the VLSI (Very Large-Scale Integration) Industry" is to provide participants with a comprehensive understanding of the industry landscape, its various roles, and the potential career opportunities.

Objectives: The students should be able to

- Understand the importance of VLSI in real world application
- Familiarise the process of chip design
- Opportunities in Analog and Digital VLSI design

Outcomes: On successful completion of program, the student should be able to:

- Identify the different steps involved in chip design
- Understand different job opportunities in VLSI.
- Understand the importance of pre silicon verification




Principal
A.J. Institute of Engineering & Technology
Mangaluru - 575 006



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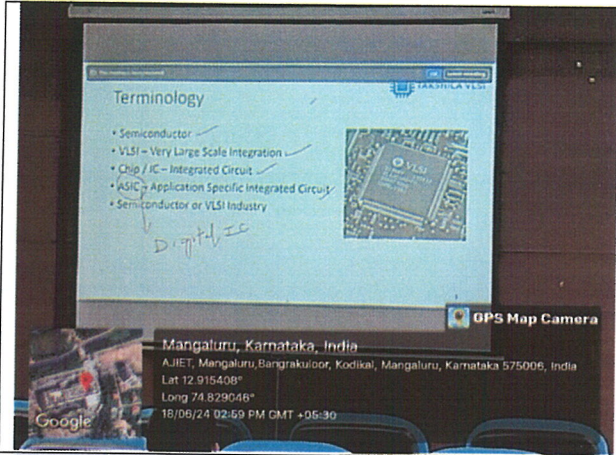
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Photos:



Mrai
Coordinator

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HOD-ECE

Dean-Academics

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